

2059500-58276001

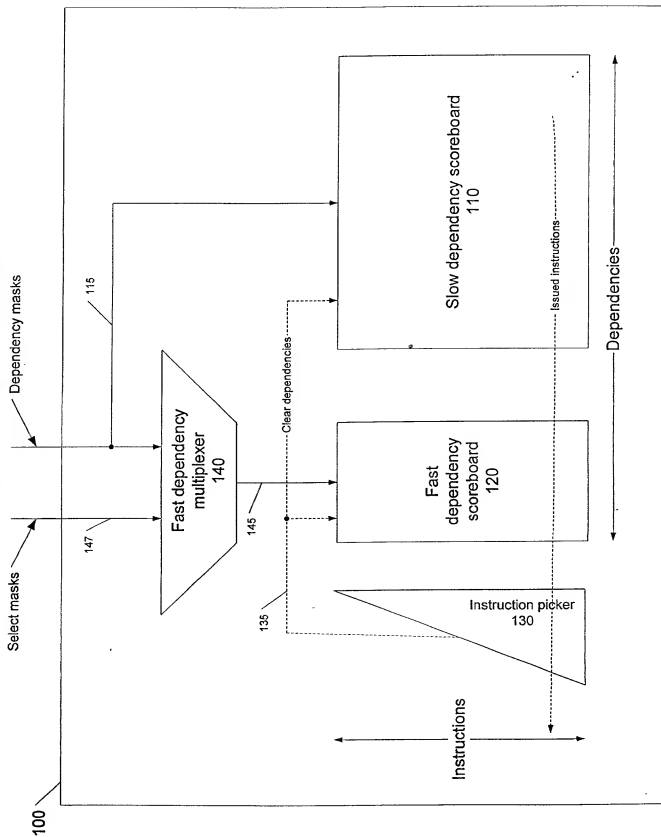


Fig. 1

2025021816001

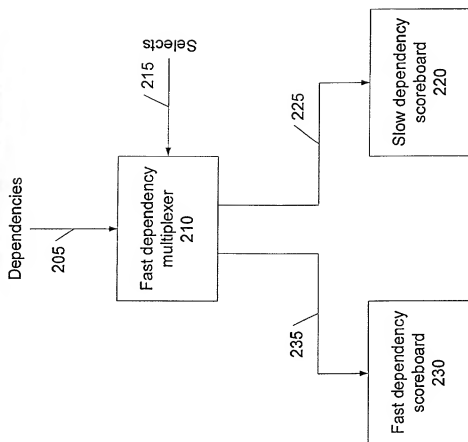
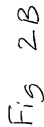


Fig. 2A



Co131		Co137	
id#	127	id#	127
id#1	127	id#1	127
id#2-29	..	id#2-29	..
id#30-127	0	id#30-127	0
id#31	127	id#31	127
id#32	31	id#32	31
id#33	31	id#33	31
id#34-61	..	id#34-61	..
id#62	31	id#62	31
id#63	61	id#63	61
id#64	63	id#64	63
id#65	63	id#65	63
id#66-93	..	id#66-93	..
id#94	63	id#94	63
id#95	63	id#95	63
id#96	95	id#96	95
id#97	95	id#97	95
id#98-125	..	id#98-125	..
id#126	95	id#126	95
id#127	95	id#127	95

Instruction IDs of the first
instruction of each octet

Attorney Docket No.: SP-5827 US
First Inventor:
Title:

128 bits

Octet	ID	127:120	119:112	111:104	103:96	95:88	87:80	79:72	71:64	63:56	55:48	47:40	39:32	31:24	23:16	15:8	7:0
0	0	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
1	8	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
2	16	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
3	24	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
4	32	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
5	40	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
6	48	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
7	56	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
8	64	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
9	72	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
10	80	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
11	88	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
12	96	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
13	104	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
14	112	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
15	120	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000

Y MULTIPLEXER

Fig. 3A

Write Pointer = 60 “Current Octet Pattern”

Write Pointer = 60 “Next Octet Pattern”

Fig 3B

[illegible]

First Inventor:

Karthik Balakrishnan

FAST INSTRUCTION DEPENDENCY MULTIPLEXER

Write Pointer = 60 “Final Select Pattern” Single strand mode

Fig 3C

[illegible]

“Final Select Pattern”

Fig 4B